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PATENT

J. Hay
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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S): Que-Won Rhee

CASE: 10991663-1

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SERIAL NO: 09/432,819

GROUP ART UNIT: 2182

AUG 22 2002

FILED: November 2, 1999

EXAMINER: Ilwoo Park

Technology Center 2100

SUBJECT: CONFIGURABLE ARCHITECTURE FOR VIRTUAL
SOCKET CLIENT TO AN ON-CHIP BUS INTERFACE
BLOCK

ASSISTANT COMMISSIONER OF PATENTS
WASHINGTON, D.C. 20231

SIR:

AMENDMENT A

In response to the Office Action dated April 23, 2002, please amend
the above-identified patent application as follows:

In the Specification:

Amend the three paragraphs found at page 5, line 20 through page
6, line 24 as follows:

The second stage of the configurable architecture for interface block
19 is implemented as a translation block 32. A clocked buffer 36 receives
and transmits control signals from/to synchronization block 31 via control
lines 41 and receives and transmits data signals from/to synchronization
block 31 via data lines 46. Clocked buffer 36 receives and transmits control
signals from/to translation block 32 via control lines 51 and receives and
transmits data signals from/to translation block 32 via data lines 46.
Translation block 32 converts the block encoding used by the virtual socket
interface protocol of specialized logic block 10 to the block encoding used by

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